

**ABSTRACT**

A double pole single throw (DPST) switch circuit including a first circuit portion corresponding to a first input port, a second circuit portion corresponding to a second input port, and an output port, wherein each of the first and second circuit portions include at least one first transistor providing a portion of an isolation channel, at least one second transistor providing a portion of a transmit channel, and at least one third transistor for providing a control bias for selecting either the transmit channel or the isolation channel.